

DC-DC Converter AVP/KEP-F

Output power up to 200 Watts

Isolated – Single-Output
for Chassis mounting



Special Features

- Electrostatic discharge: 8kV contact (chassis), 15 kV air, (level 4) according to EN 61000-4-2:1995
- Fast transients (Burst): 2 kV (level 3) / (criterion A) / according to EN 61000-4-4:2004
- Surge: Input immunity 2 kV sym./asym. criterion A according to EN 61000-4-5:2006
- Conducted immunity 10V/m according to EN 61000-4-6:1996
- Conducted emission: Input filtering according to EN 55022:2006 class B***
- Zero load operation and short circuit protection
- Overtemperature shutdown
- Remote off (EN) with TTL – L-signal
- Overvoltage protection in the output, even in case of external supply (OVP)
- Reverse polarity protection by internal fuse (diode at $V_{in} = 110V$)
- Extremely low thermal stress of sensitive components due to dissipated power loss over lateral heatsink
- Yellow LED indicate operating mode

Technology

- Power section in 160kHz-MOS-FET-technology
- Regulator section in SMT
- Coated assembly
- Coated and glued parts for better vibration resistance

Specifications

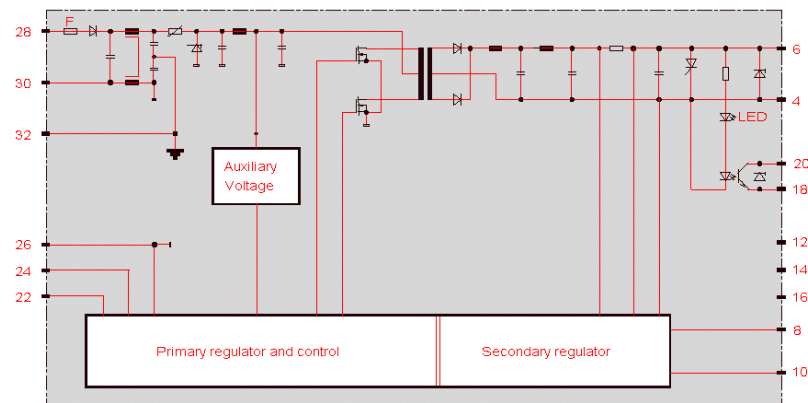
at $v_{amb}=25^{\circ}C$, $V_{out nom}$, $I_{out nom}$

Temperature	
Ambient air	$v_{amb} = -40^{\circ}C...+85^{\circ}C$
Storage	$v_S = -40^{\circ}C...+100^{\circ}C$
Rise inside chassis	$\Delta v_{Ci} \leq 20K$
Rise on heat sink	$\Delta v_K \leq 35K$
Output voltages (output 1)	
Tolerance	$\Delta V_{out} \leq \pm 0,5\%^*$
Ripple at $v_{amb} = -40^{\circ}C...+85^{\circ}C$	$V_{out ripple} \leq 3,5\%$
Temperature coefficient	$TC \leq 0,016\%/K$
Regulation at $v_{amb} = -40^{\circ}C...+85^{\circ}C$	
Line reg. for $V_{in range}$	$\Delta V_{out} \leq 2mV$
Load reg. static	$\Delta V_{out} \leq 10mV/A$
Load change (25°C)**	$\Delta V_{out} \leq 38 (23)mV/A$
Output "Power Good"	
Admissible voltage	$V_{CEO} \leq 24 V$
Admissible current	$I_C \leq 20 mA$
Saturation voltage	$V_{CE(sat)} \leq 1,2 V$
OVP	
Starting point /%	$V_{out off} \leq 130\% V_{out nom}$
Admissible continuous ext. current	$I_{ext} \leq 6A$
Isolation – voltage strength	
Input to output	$V_{iso i/o} \geq 1,5kVrms$
Resistance	$R_{iso} \geq 10^{11}\Omega$
Capacitance	$C_{iso} \leq 4700nF$
Input to case	$V_{iso i/c} \geq 1,5 kVrms$
Output to case	$V_{iso o/c} \geq 0,5 kVrms$
Degrees of protection (inserted in rack)	= IP10***
Weight AVP/KEP-F	M ca. 1200g

Block Diagram

- 4 = $-V_{out1}$
- 6 = $+V_{out1}$
- 8 = +S (Sense)
- 10 = -S (Sense)
- 12 = n.c.
- 14 = n.c.
- 16 = n.c.
- 18 = -PG (-Power Good)
- 20 = +PG (+Power Good)
- 22 = TR (Tracking)
- 24 = EN (On/Off)
- 26 = GND (TR and EN)
- 28 = $+V_{in}$
- 30 = $-V_{in}$
- 32 = ∇ / \oplus

Sense connection is not required.



* $I_{out min} = 0,1 I_{out nom}$

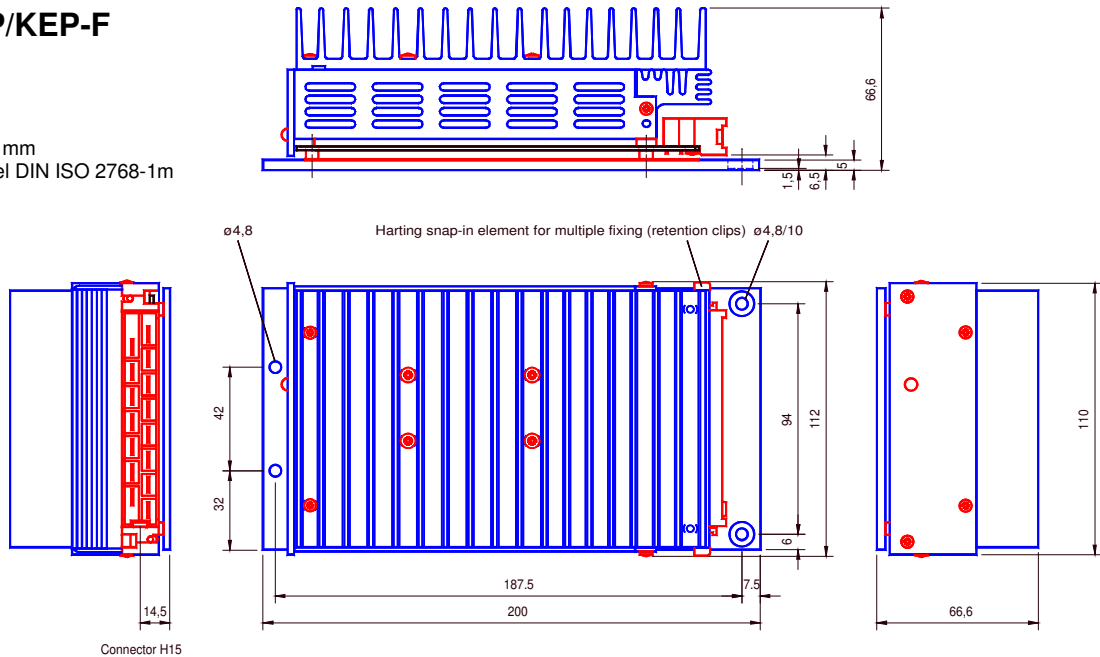
** Higher degrees of protection by properly mounting

*** In built-in condition our devices may show different EMC properties

Drawing AVP/KEP-F

Dimensions in mm

Tolerance: general ± 1 mm
Front panel DIN ISO 2768-1m



Mounting/Operating Instructions

Installation: The converters have to be installed according to the guidelines currently in force, like other open electronic component assemblies. Attention must be paid to sufficient ventilation, fastening and protection against accidental contact! Plug in not under voltage if converter connected parallel or in series.

Reverse polarity protection: The converters are equipped with a soldered-in time-lag fuse corresponding to IEC 127-2 for input protection. For rating of fuse refer to listing below. Pay attention on sufficient current of current source in case of short-circuit.

Connector pin 32 (∇/\oplus) - Equipotentiality/PE: This pin has to be properly connected in order to assure operation.

Excess temperature protection: In case of inside temperatures exceeding $>101^\circ\text{C}$, typ. 105°C , (due to inadmissible operation conditions) the output voltages are automatically switched off and restarted after cooling down about 10K.

External shutdown (EN): $V < 0,8$ V at pin 24 (EN) to pin 26 or connecting an active transistor with open collector to this pins switches off the output. $I_{\text{source}} 500 \mu\text{A}$

Overvoltage protection: Externally or internally caused overvoltage at the output leads to a thyristor controlled short-circuit and shut-down of the output. After elimination of the overvoltage the output voltage restarts automatically latest after 1s.

Output voltage monitoring (Power Good): Simultan to the lighting of a LED a transistor with open-collector switches on. Level V_{PG} see table below.

Current limiting: $I_{\text{out lim}} = 1,1 \dots 1,2 I_{\text{out nom}}$. At more than 1,5 $I_{\text{out nom}}$, the output switches off and restarts automatically latest after 5s of elimination of the overload.

Capacitive load: When switching on (by connecting to the input voltage or by means of a control signal at the input "EN") the DC-DC converters can drive capacitive loads up to 100 mF parallel to the nominal load. At overload conditions the switching on period takes approx. 375ms. After elimination of the overload the output voltage restarts automatically latest after 5s.

Tracking operation: If the pins 22 as well as 26 of two or more converters are connected, the output voltages in case of short-circuit or overload go synchronously down and restart at the same moment.

Sense operation: Sense connection is not required. If it is accomplished, the voltage at the load is reduced by approx. 100 mV. The voltage drop on the interconnection leads between the converter and the load should not exceed 0,5 V.

Standard converters AVP/KEP-F

V_{out} V	I_{out} A	η ²⁾ %	Type	V_{PG} ³⁾ VDC	$V_{in\ nom}$ VDC	$V_{in\ operating}$ VDC	$V_{in\ range}$ VDC	$I_{in\ max}$ A	I_{rat} A	Order Number
12	12 ¹⁾	85		AVP/KEP-F	>10	72	50...90	43...101	4	
15	10 ¹⁾	85	AVP/KEP-F	>13	72	50...90	43...101	4,3	6,3	09 52 23 0102 6
15	10 ¹⁾	87	AVP/KEP-F	>13	110	77...138	66...154 ⁴⁾	2,6	5	09 52 73 0102 5
24	6 ¹⁾	84	AVP/KEP-F	>21	24	16...30	15...36	11,5	16	09 52 94 0102 9
24	6 ¹⁾	83	AVP/KEP-F	>21	36	25...45	19...51	9,5	16	09 52 34 0102 3
24	9 ⁵⁾⁶⁾	87	AVP/KEP-F	>21	110	77...138	66...154 ⁴⁾	3,5	5	09 52 74 0102 4

Order numbers for option "EMC fingerstrips" and other options on request

- 1) Derating between 70 °C and 85 °C: 4%/°C
- 2) At $V_{in\ nom}$; typical
- 3) V_{PG} = Switching point for the output level for "Power Good"
- 4) 176 V for 100 ms permissible
- 5) $I_{out\ lim} = 1,05 \dots 1,15 I_{out\ nom}$
- 6) Derating between 60 °C and 70 °C: 0,1 A / °C; 70 °C and 85 °C: 0,32 A / °C

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DIN EN ISO 9001:2000 certified